

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q77597

Yasushi KINOSHITA

Appln. No.: 10/669,655

Group Art Unit: 2811

Confirmation No.: 5578

Examiner: Samuel A. GEBREMARIAM

Filed: September 25, 2003

For: SEMICONDUCTOR INTEGRATED CIRCUIT

**PETITION FOR EXTENSION OF TIME UNDER 37 C.F.R. § 1.136**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Sir:

Pursuant to 37 C.F.R. § 1.136, Applicant hereby petitions for an extension of time of one months, extending the time for responding to the Office Action of June 22, 2007 to October 22, 2007.

The statutory fee of 120.00 is being submitted via EFS. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this sheet is enclosed.

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Respectfully submitted,  
/Howard L. Bernstein/  
Howard L. Bernstein  
Registration No. 25,665

WASHINGTON OFFICE

**23373**

CUSTOMER NUMBER

Date: October 22, 2007